

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Wayne P. Bailey on 6/24/2009.

The application has been amended as follows:

As per claim 1:

--wherein verification of a checksum is unnecessary if the data packet arrived over a physical network adapter that supports checksum offload and the checksum is verified to be good-- was inserted before the period in the last line of claim 1.

As per claim 15:

--wherein verification of a checksum is unnecessary if the data packet arrived over a physical network adapter that supports checksum offload and the checksum is verified to be good-- was inserted before the period in the last line of claim 15.

As per claim 29:

--wherein verification of a checksum is unnecessary if the data packet arrived over a physical network adapter that supports checksum offload and the checksum is verified to be good-- was inserted before the period in the last line of claim 29.

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The following is an examiner's statement of reasons for allowance: The Priro Art of Record and, in particular, Thompson; Michael I. et al. (US 5430842 A) teaches responsive to receiving the data packet at a first partition of a data processing system from a second partition of a data processing system, identifying a state of a first flag and a state of a second flag in the data packet (Col. 3, lines 40-49 and col. 8, lines 13-27 of Thompson teaches checksum control information including first flag 74 and second flag 72, responsive to receiving the data packet at a first partition 22 in Figure 2 of Thompson of a data processing system from a second partition 12 in Figure 2 of Thompson of a data processing system, is used for identifying a state of first flag 74 and a state of second flag 72 in the data packet in order to verify data; and selectively verifying a checksum, by the first partition in the logical partitioned data processing system, for the data packet as indicated by the state of the first flag and the state of the second flag (Col. 3, lines 40-49 and col. 8, lines 13-27 of Thompson), wherein the first flag and the second flag are both checksum-based flags that indicate checksum characteristics associated with the data packet (col. 8, lines 13-27 of Thompson). Hefferon, in an analogous art, teaches use of a logical partitioned data processing system (Abstract in Hefferon). --wherein verification of a checksum is unnecessary if the data packet arrived over a physical network adapter that supports checksum offload and the checksum is verified to be good-- sets for the context for the method of claim 1, a context which is not relevant in the Prior Art. As per claim 15, the system for using and/or implementing the method of claim 1 recited in the context of --wherein verification of a checksum is unnecessary if the data packet arrived over a physical

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network adapter that supports checksum offload and the checksum is verified to be good-- is also allowable since the System implements the method of claim 1 in the same context and the method is allowable. As per claim 29, the method implemented in computer instruction, i.e., a computer implemented method of claim 1 recited in the context of --wherein verification of a checksum is unnecessary if the data packet arrived over a physical network adapter that supports checksum offload and the checksum is verified to be good-- is also allowable since the method of claim 1 in the same context is allowable.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph D. Torres whose telephone number is (571) 272-3829. The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Scott T. Baderman can be reached on (571) 272-3644. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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